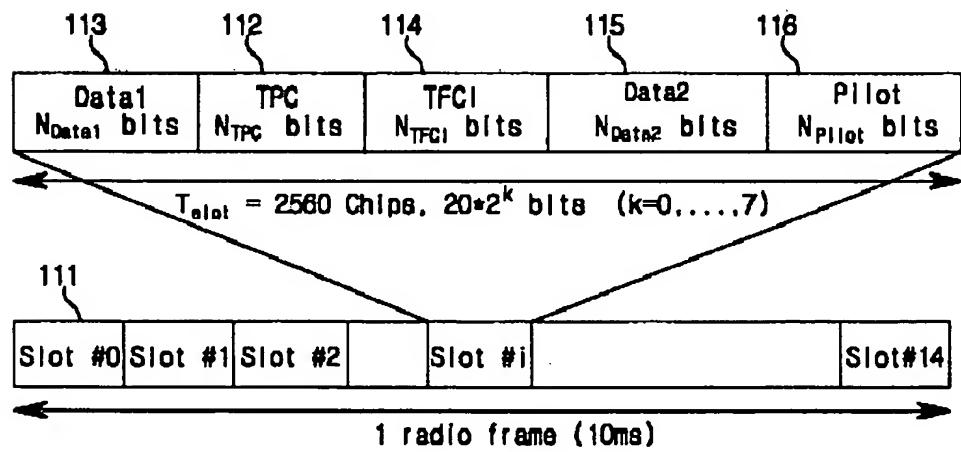


**FIG. 1A
(PRIOR ART)**

prior art reference to FIG. 1B



**FIG. 1B
(PRIOR ART)**

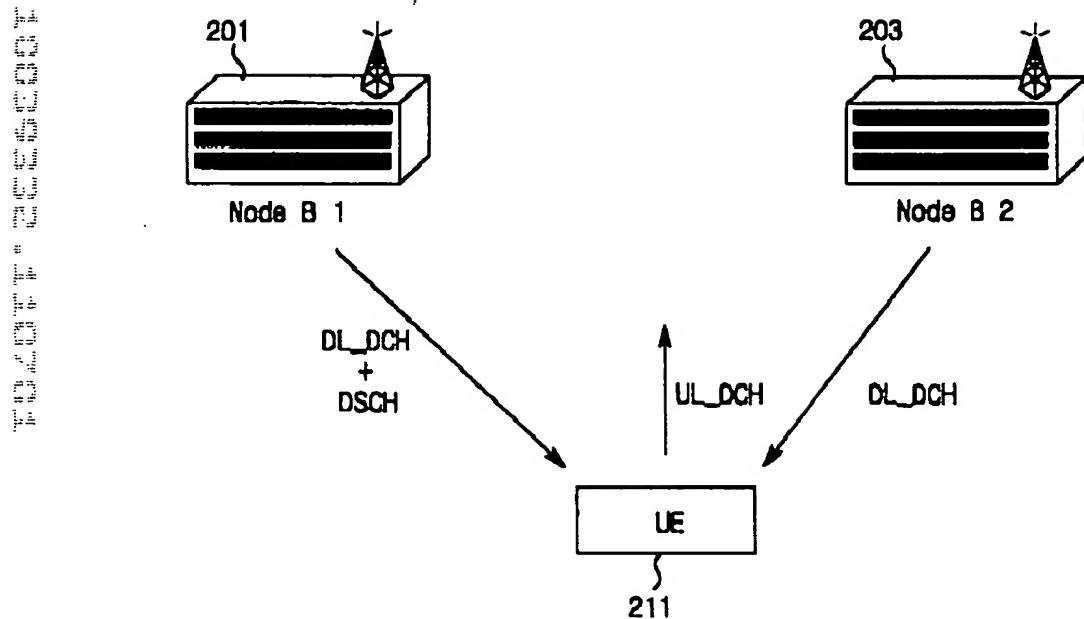


FIG. 2
(PRIOR ART)

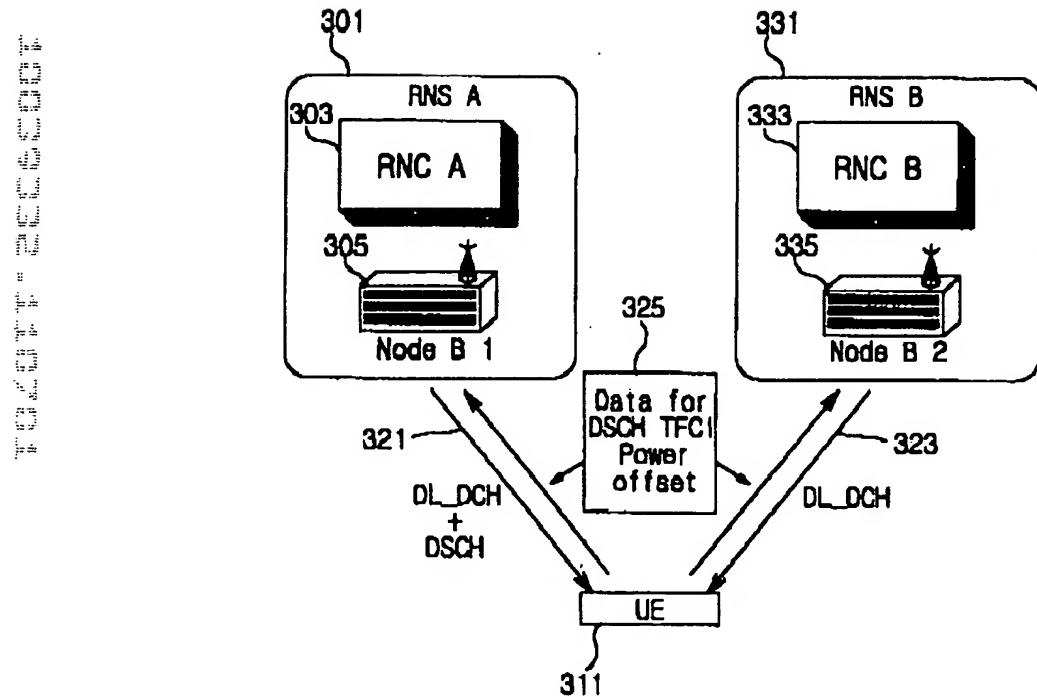


FIG. 3
(PRIOR ART)

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SPR GS 2000/00000000000000000000000000000000

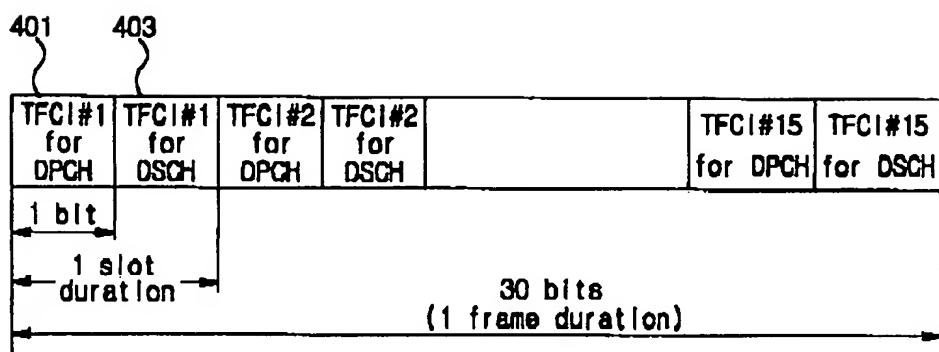


FIG. 4

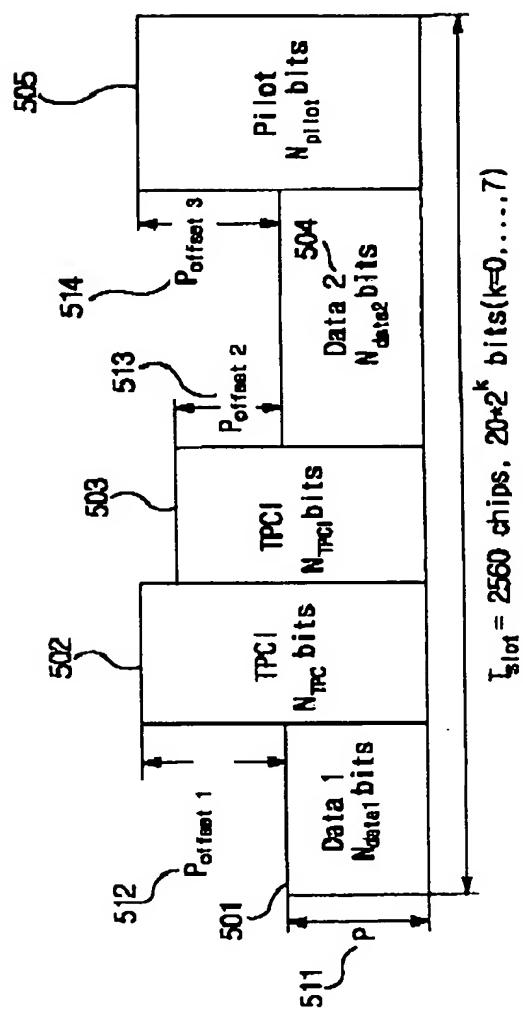


FIG. 5

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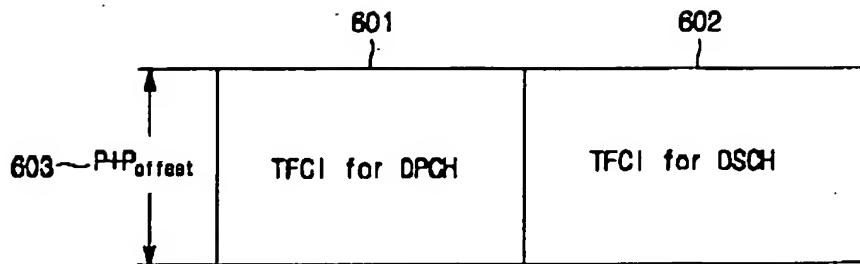


FIG. 6A

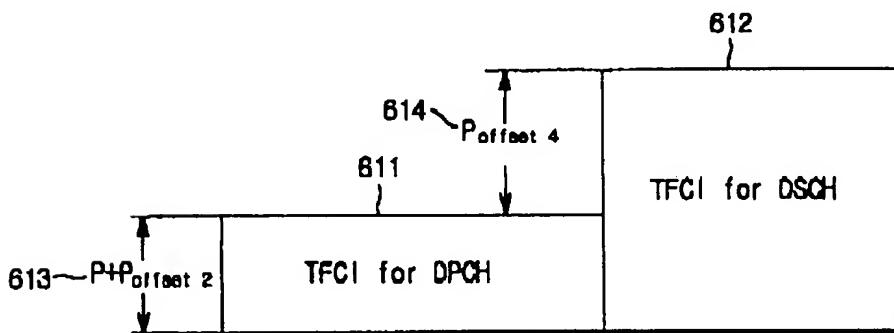


FIG. 6B

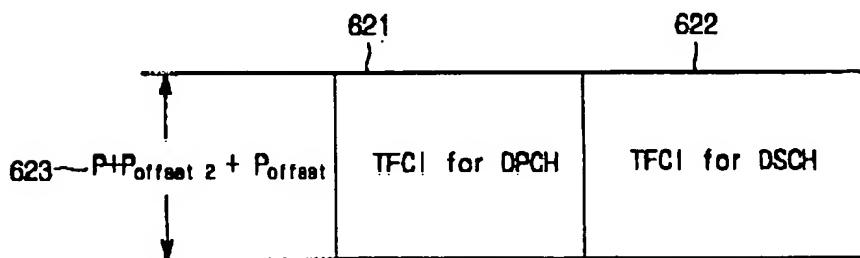


FIG. 6C

701
721
Node B 1
DL_DCH + DSCH
751
Code word
for
transmission
power offset
of TFCI for
DSCH
703
731
Node B 2
DL_DCH
711
UE

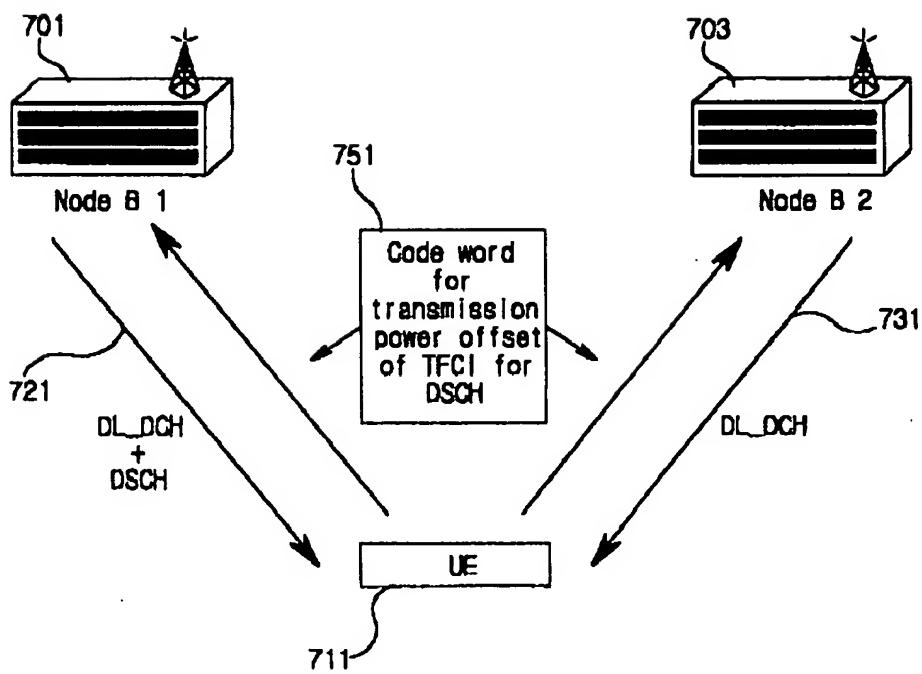


FIG. 7

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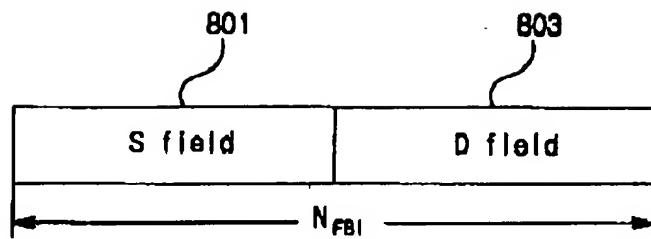


FIG. 8A

Proprietary Information of Qualcomm Inc.

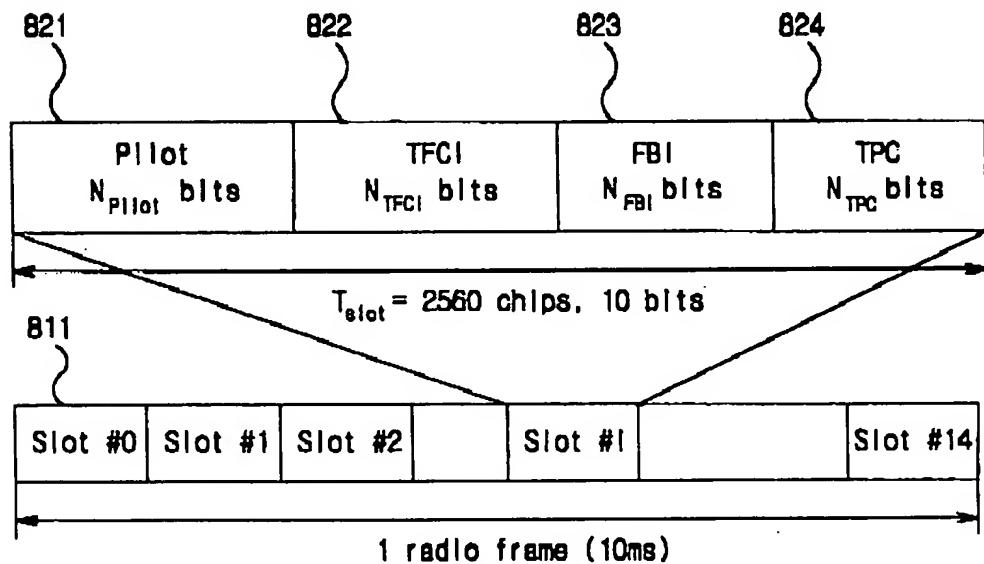


FIG. 8B

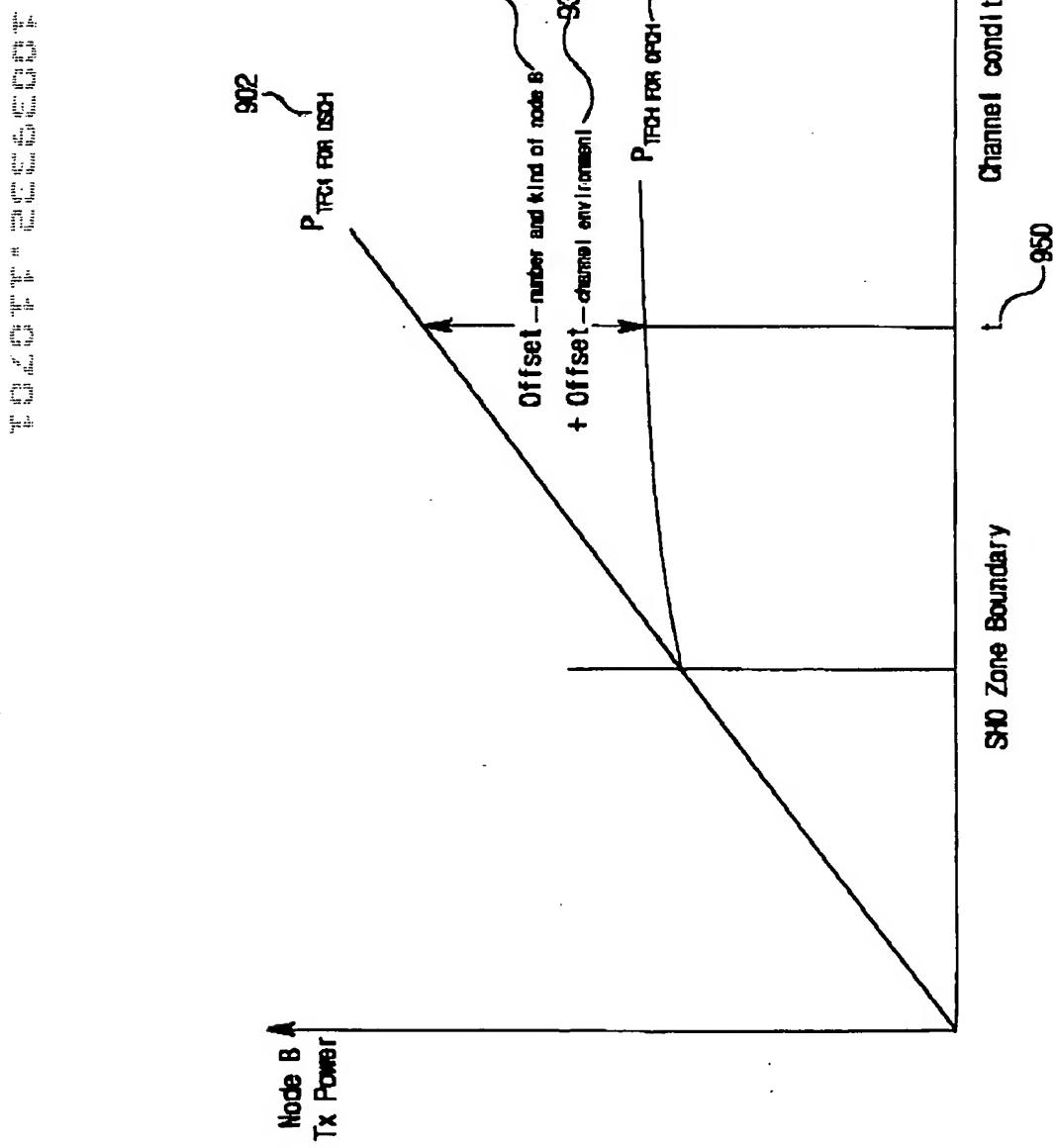


FIG. 9

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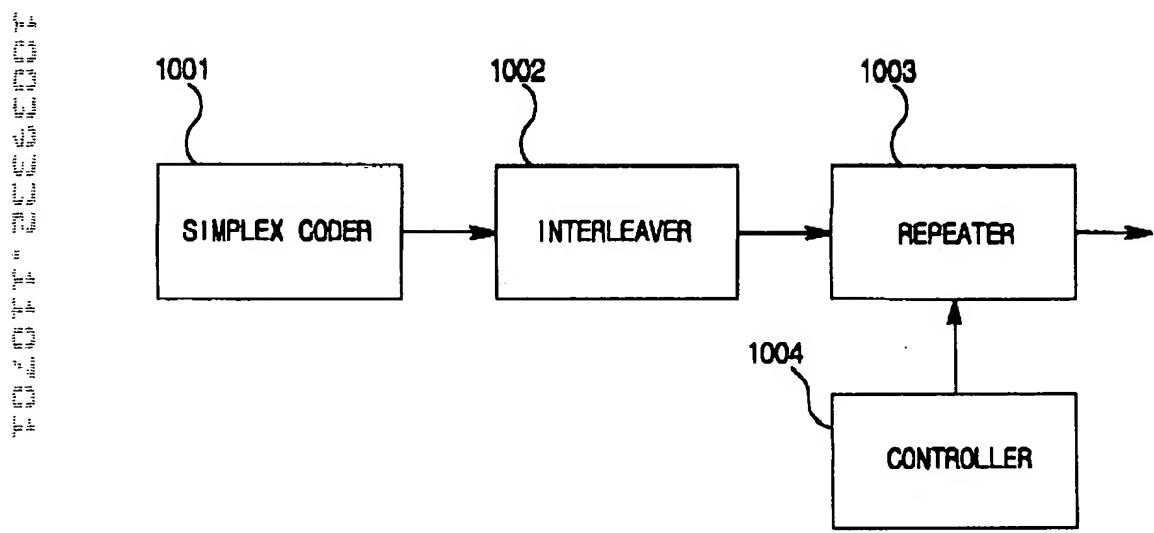


FIG. 10

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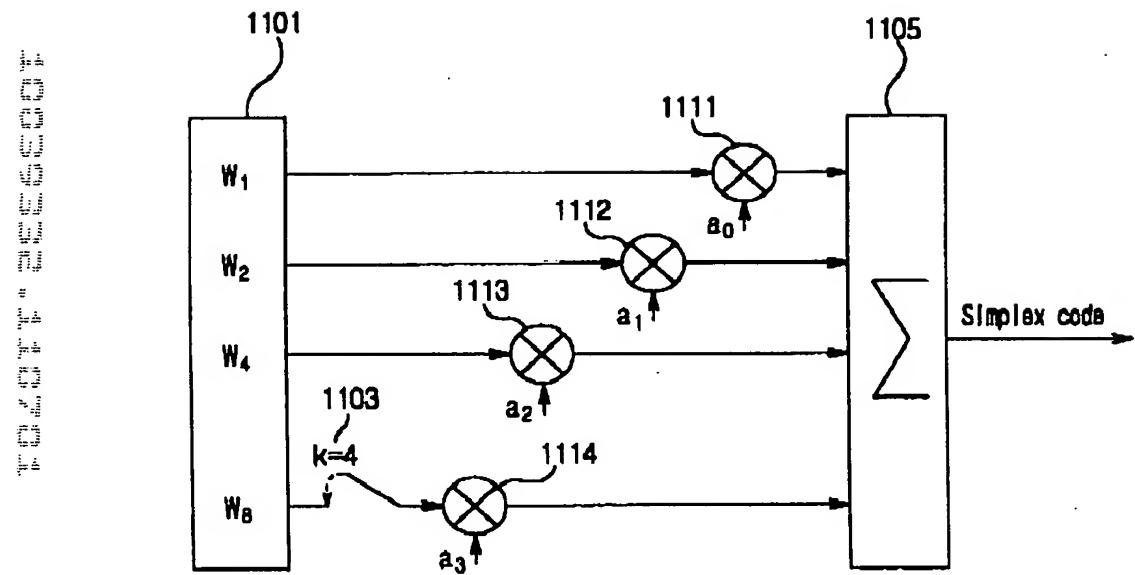


FIG. 11

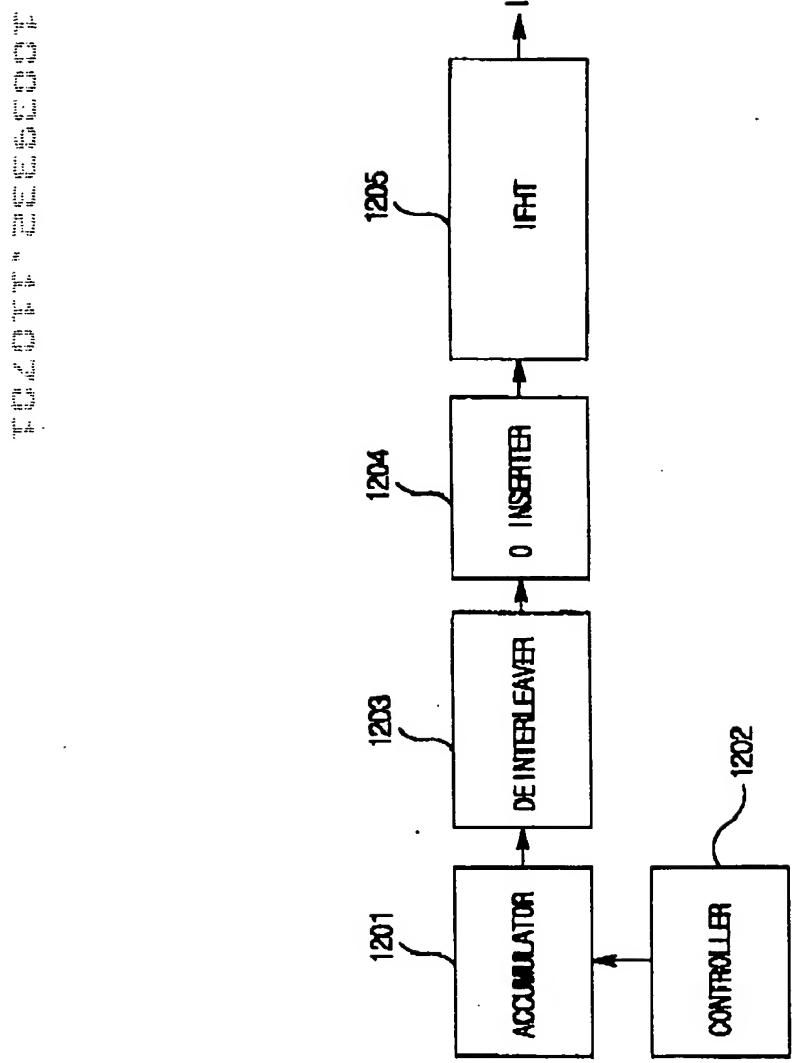


FIG. 12

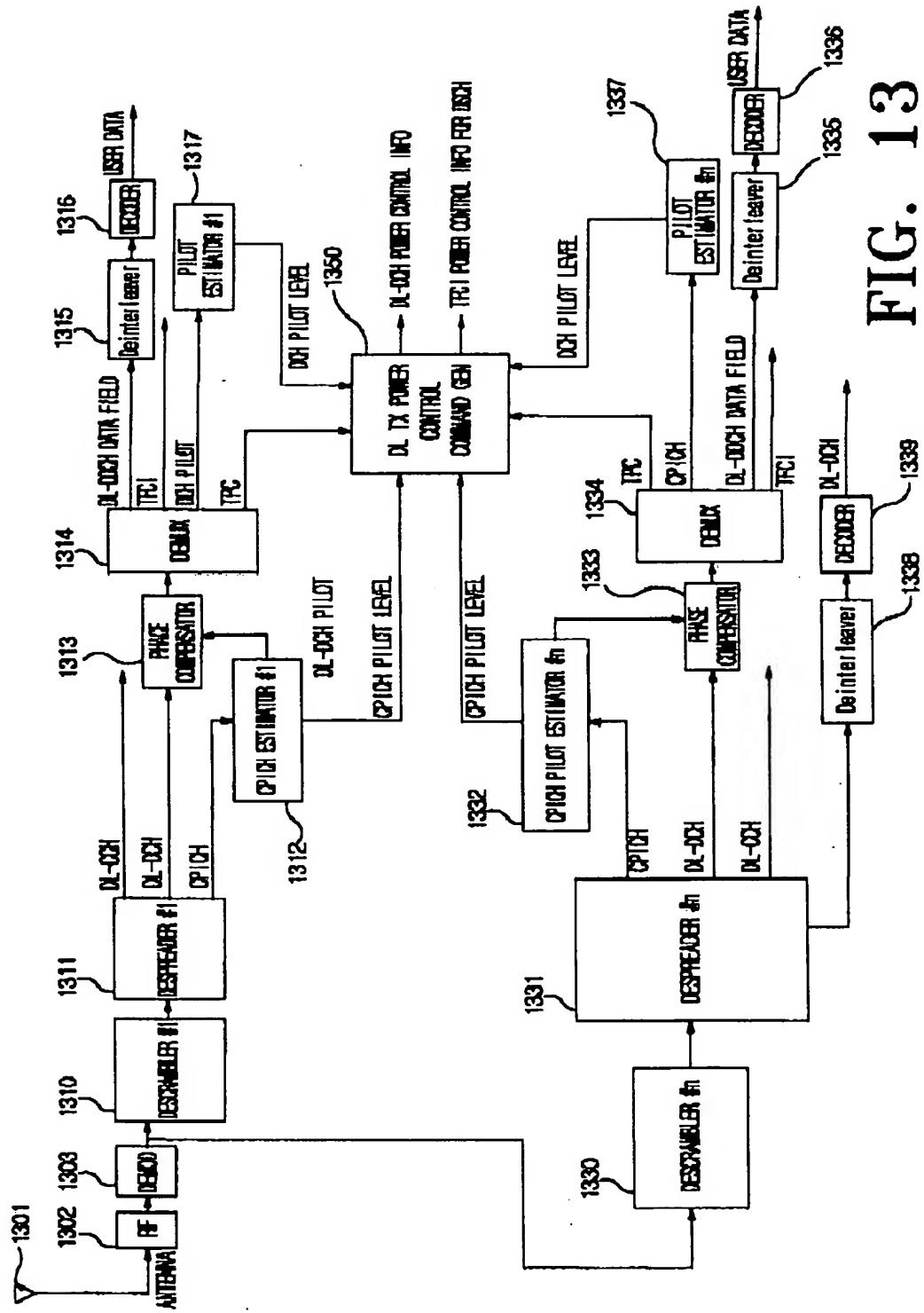
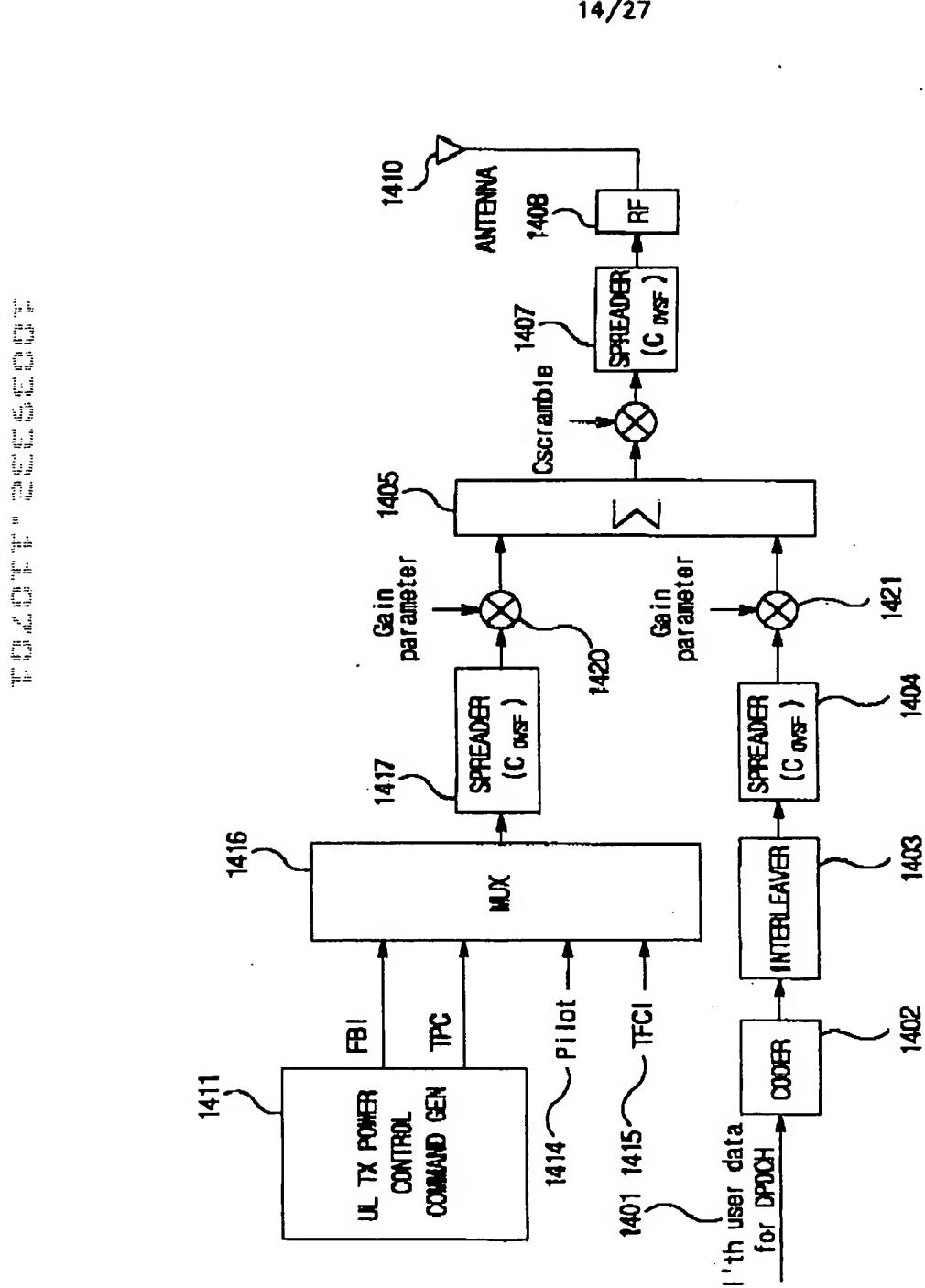


FIG. 13

FIG. 14



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FIG. 15

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```

graph TD
    Antenna((ANTENNA)) -- 1501 --> RFF[RFF]
    RFF -- 1502 --> DPD[DPD]
    DPD -- 1503 --> EST[ESTIMATOR]
    EST -- 1504 --> DECODER[DECODER]
    DECODER -- 1505 --> DEBLOCKER[DEBLOCKER]
    DEBLOCKER -- 1506 --> DPA[DPA]
    DPA -- 1507 --> DCHPilotEstimator[DCH PILOT ESTIMATOR]
    DCHPilotEstimator -- 1508 --> ULTXPowerControl[UL TX POWER CONTROL]
    ULTXPowerControl -- TPC for Uplink transmission --> TPC[TPC]
    TPC -- 1509 --> ULTXPowerController[UL TX POWER CONTROLLER]
    ULTXPowerController -- UL-DCH POWER CONTROL --> ULTXPowerController
    ULTXPowerController -- TPC POWER CONTROL COMMAND FOR DCH --> ULTXPowerControl
    ULTXPowerController -- 1510 --> PhaseComp[PHASE COMPENSATOR]
    PhaseComp -- 1511 --> Interleaver[INTERLEAVER]
    Interleaver -- 1512 --> Decoder[DECODER]
    Decoder -- USER DATA --> BaseStation[Base Station]
  
```

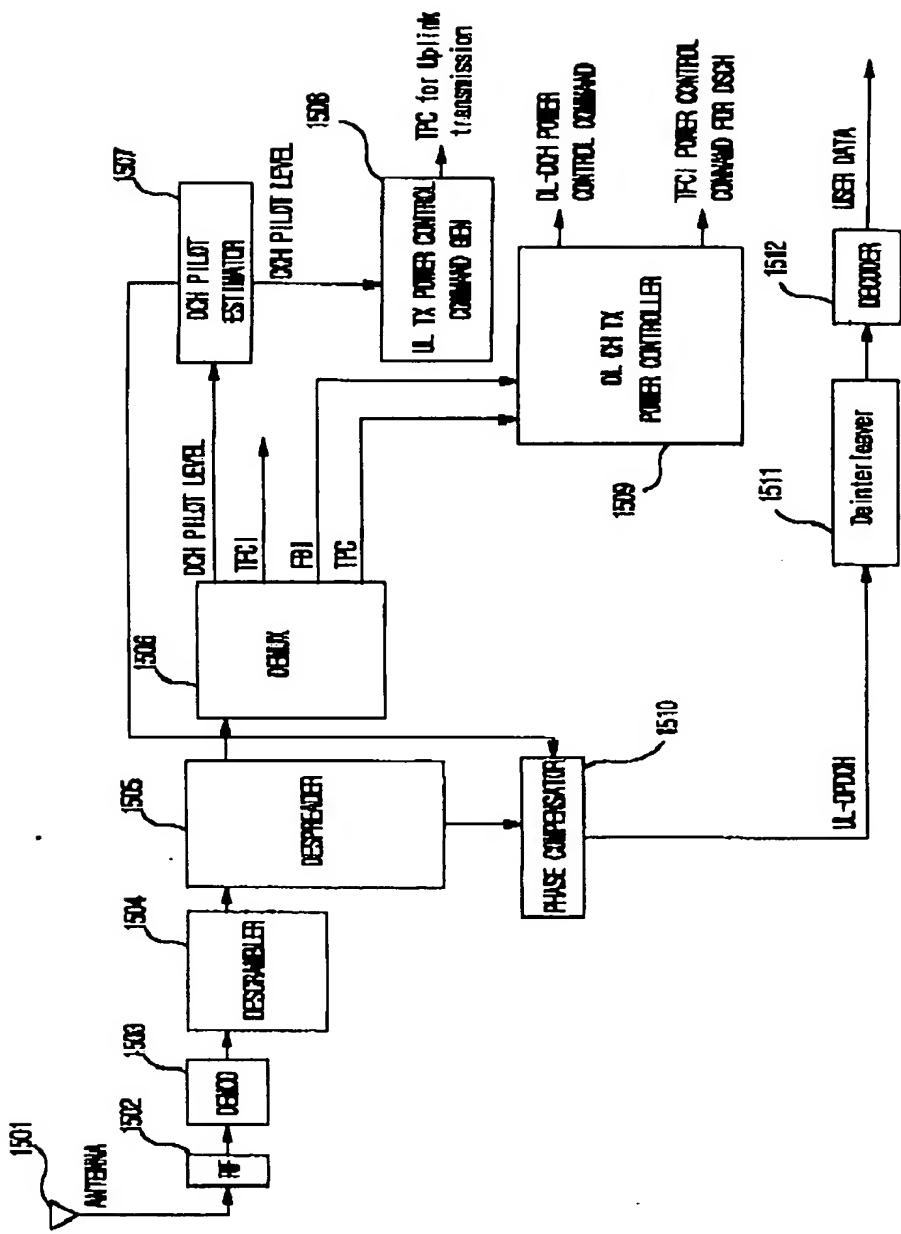
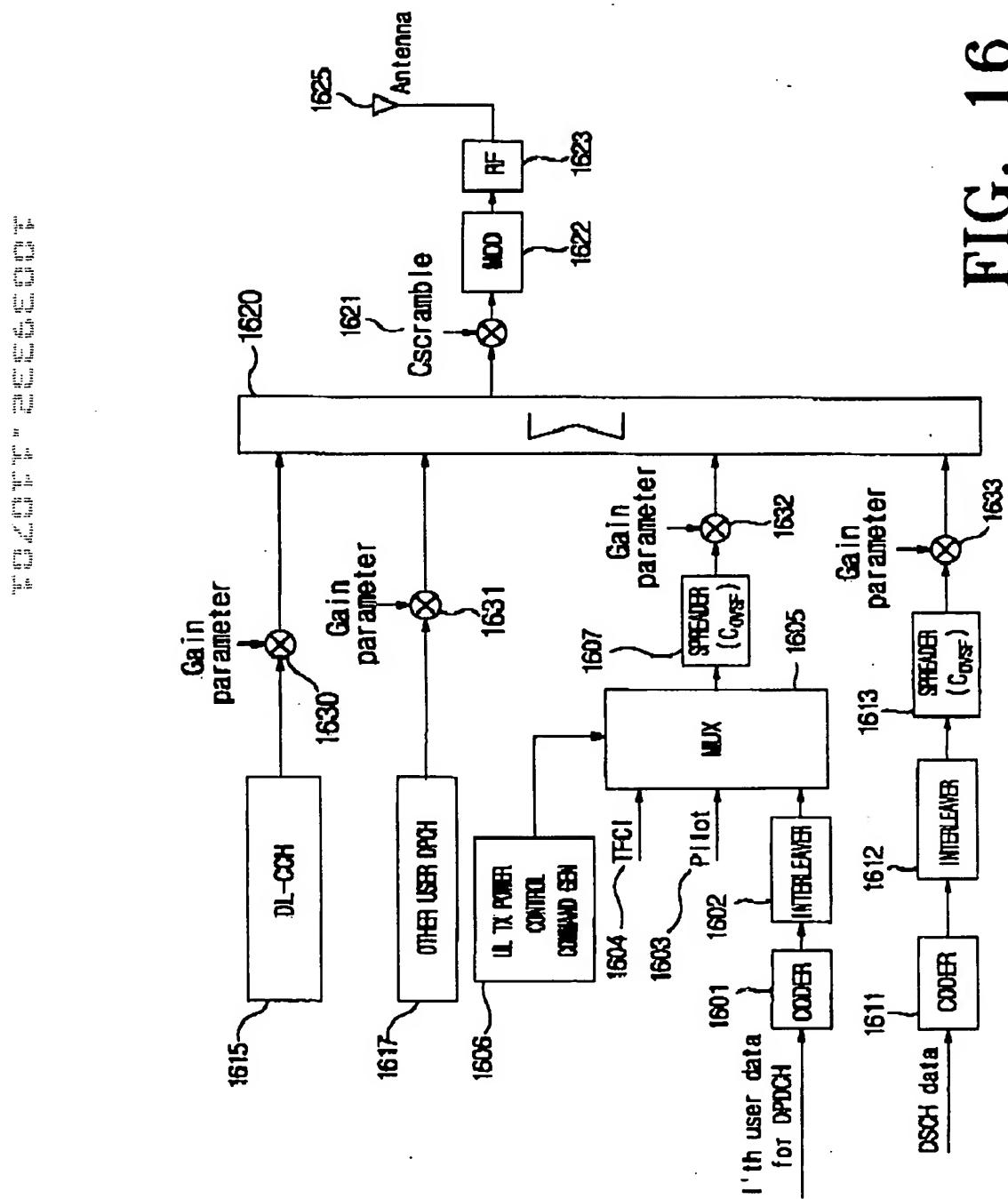


FIG. 16



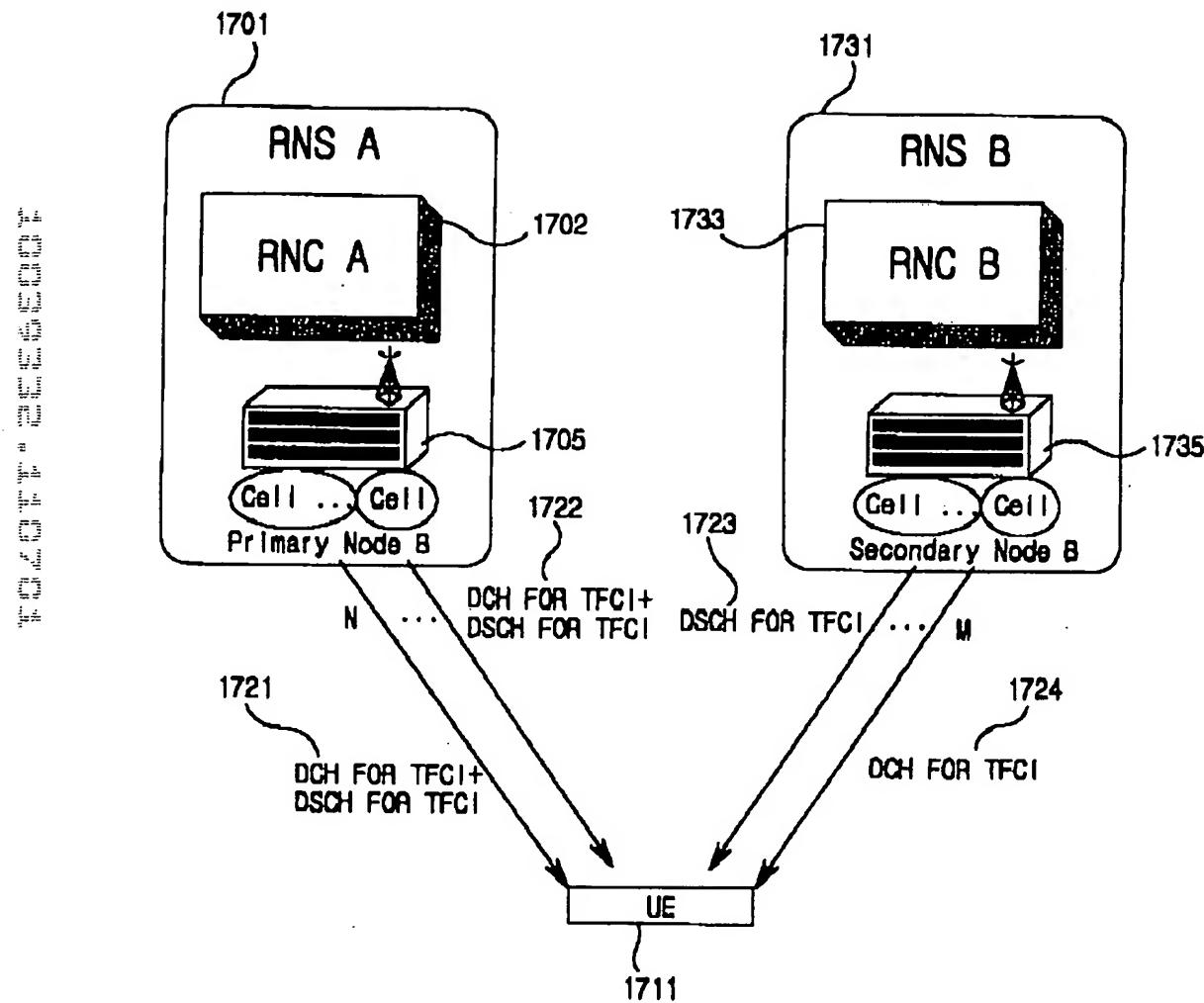


FIG. 17

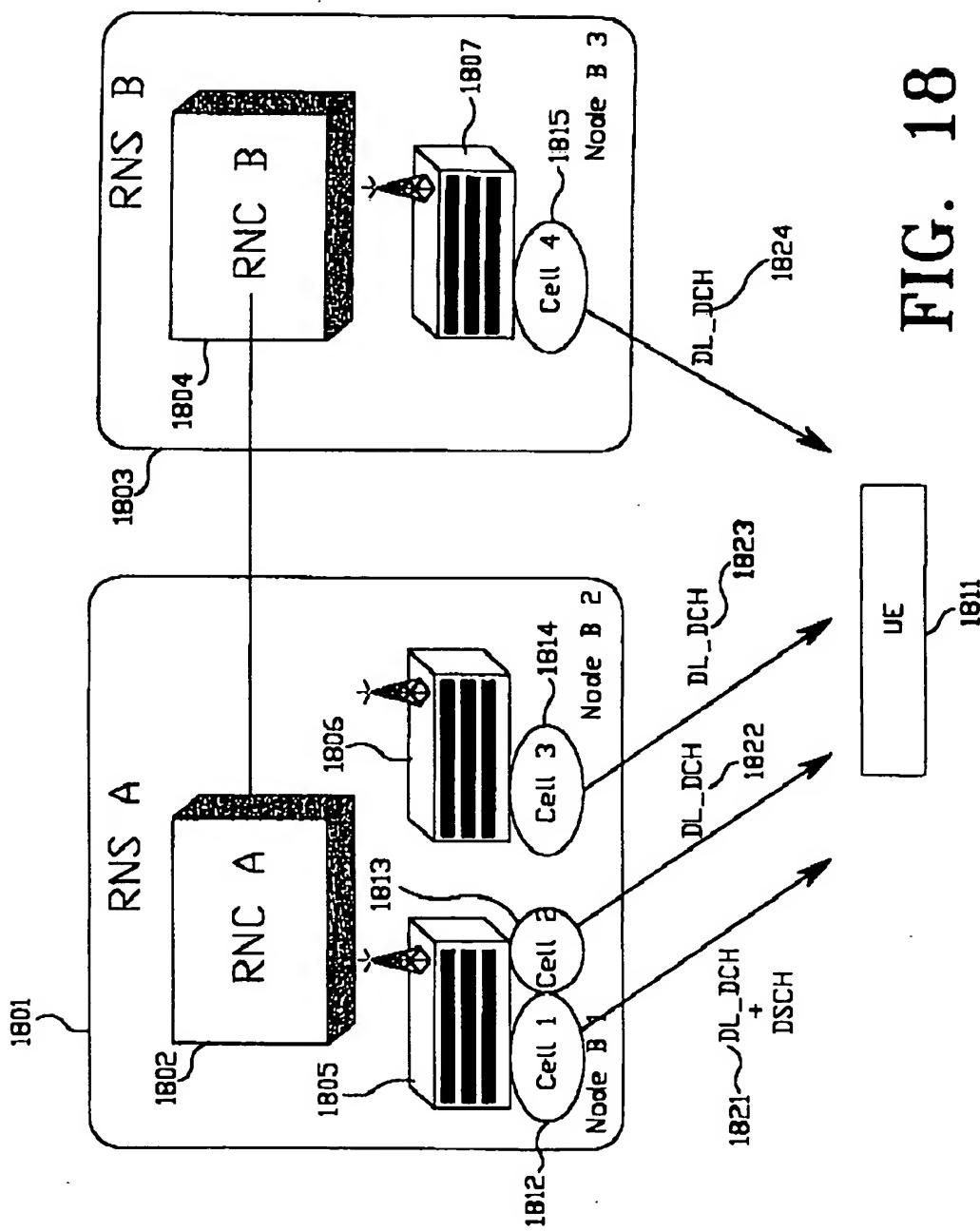


FIG. 18

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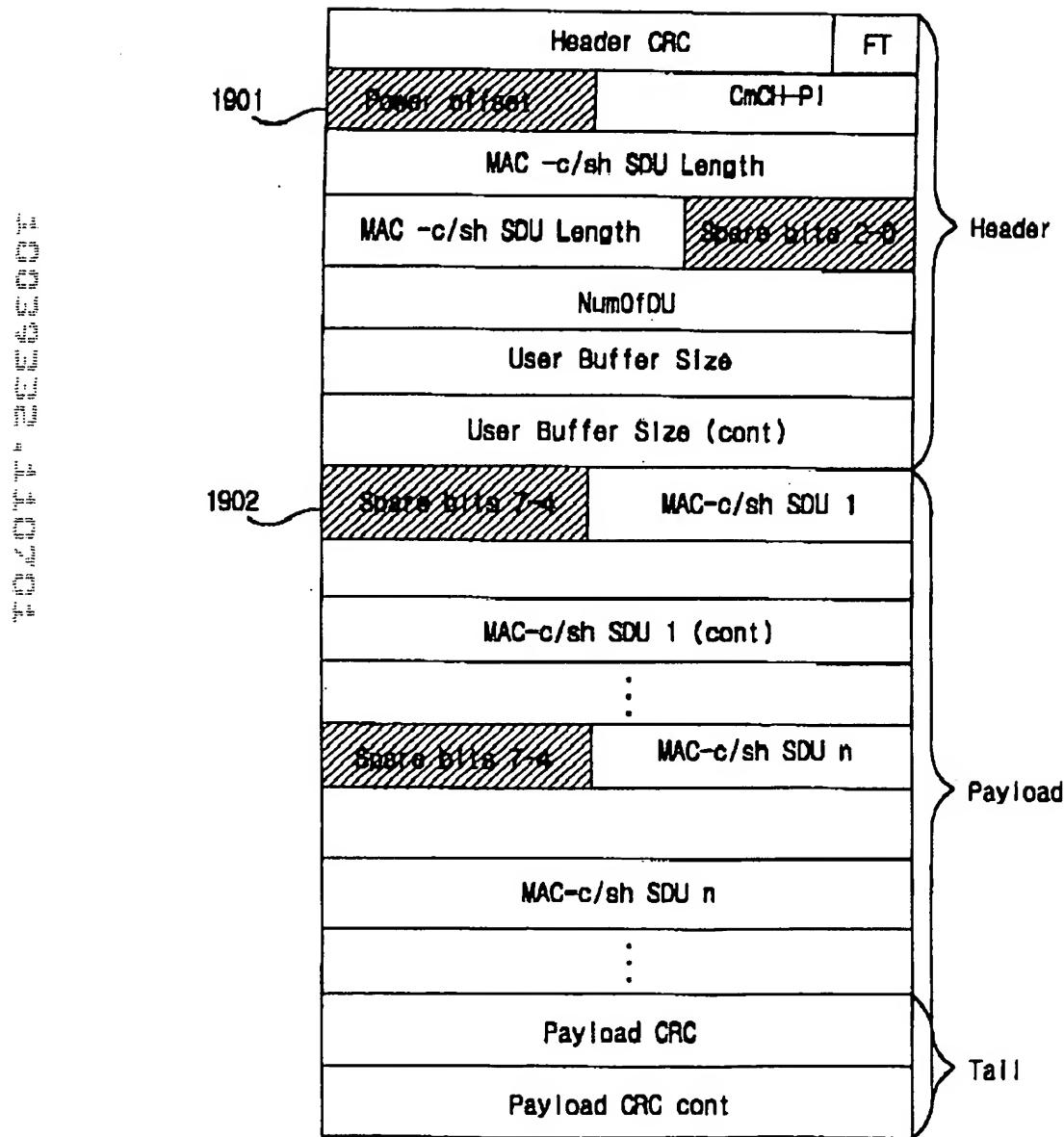


FIG. 19

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7
0
CFN
POWER OFF SET
Spare Extension

FIG. 20

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2101

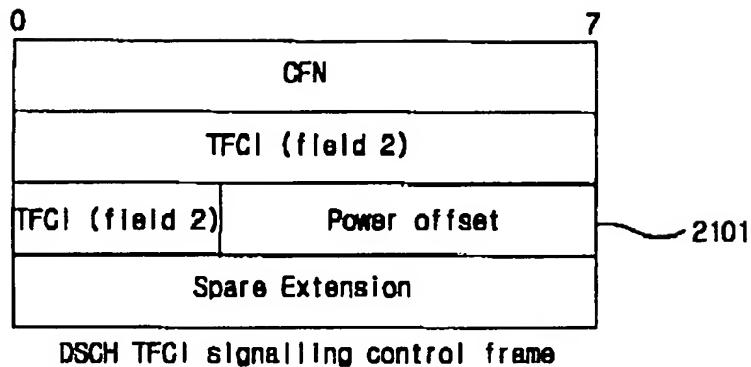


FIG. 21

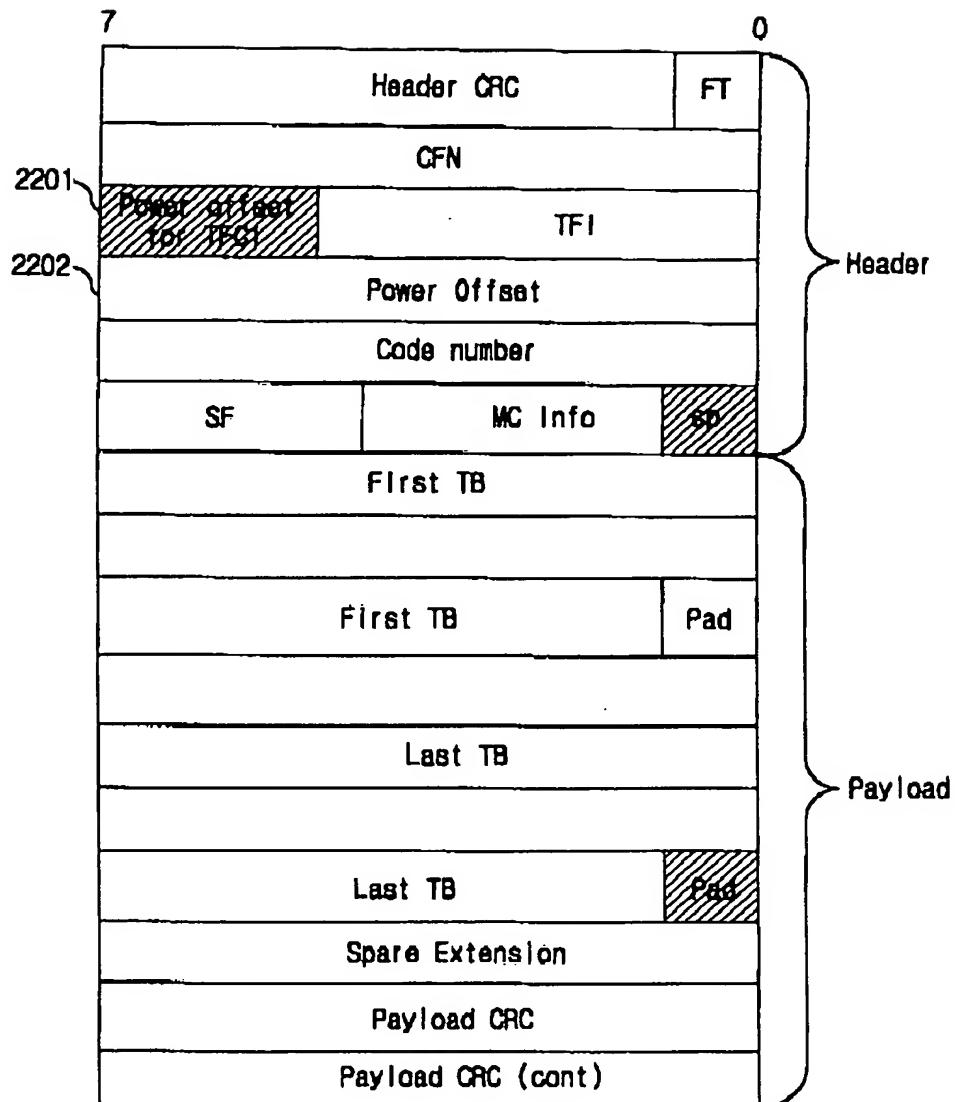


FIG. 22

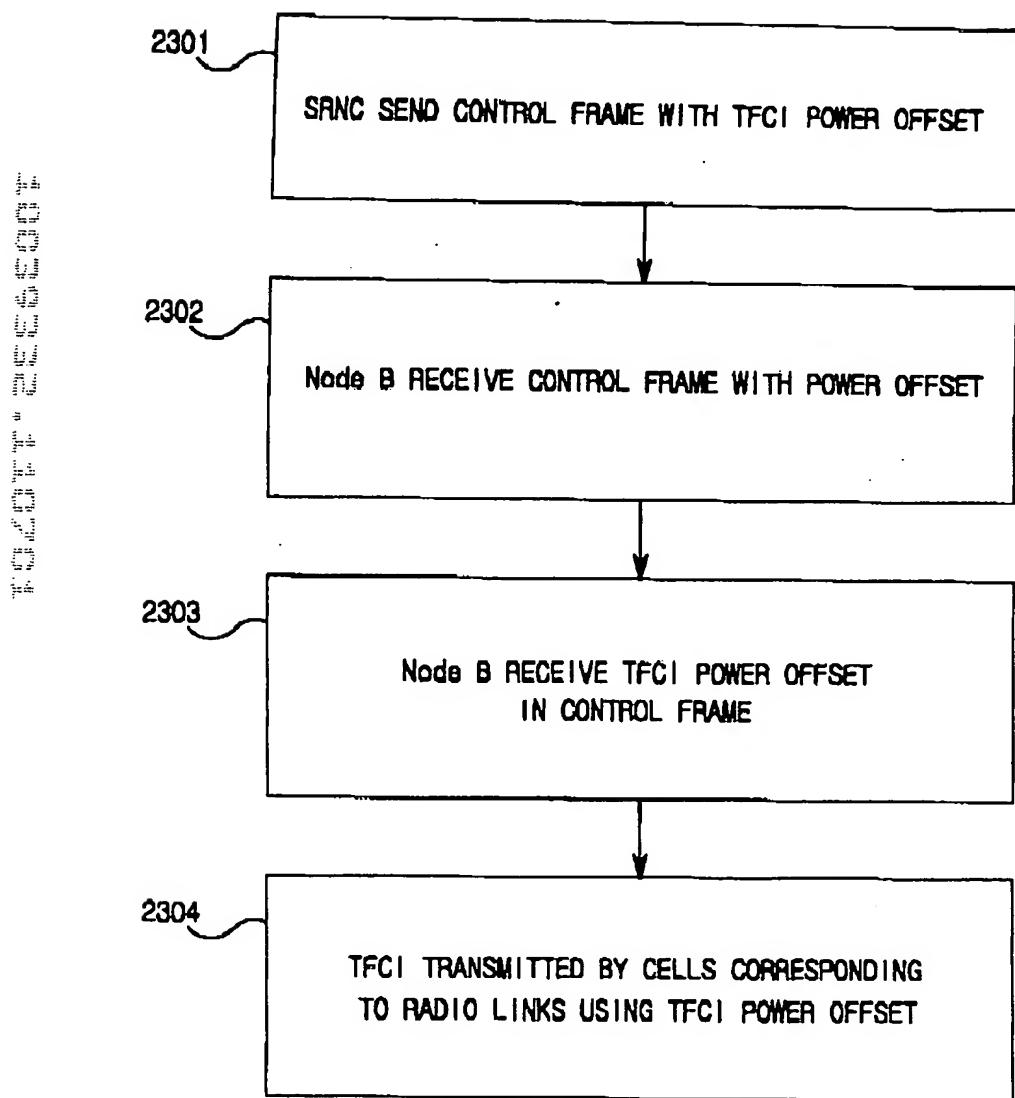


FIG. 23

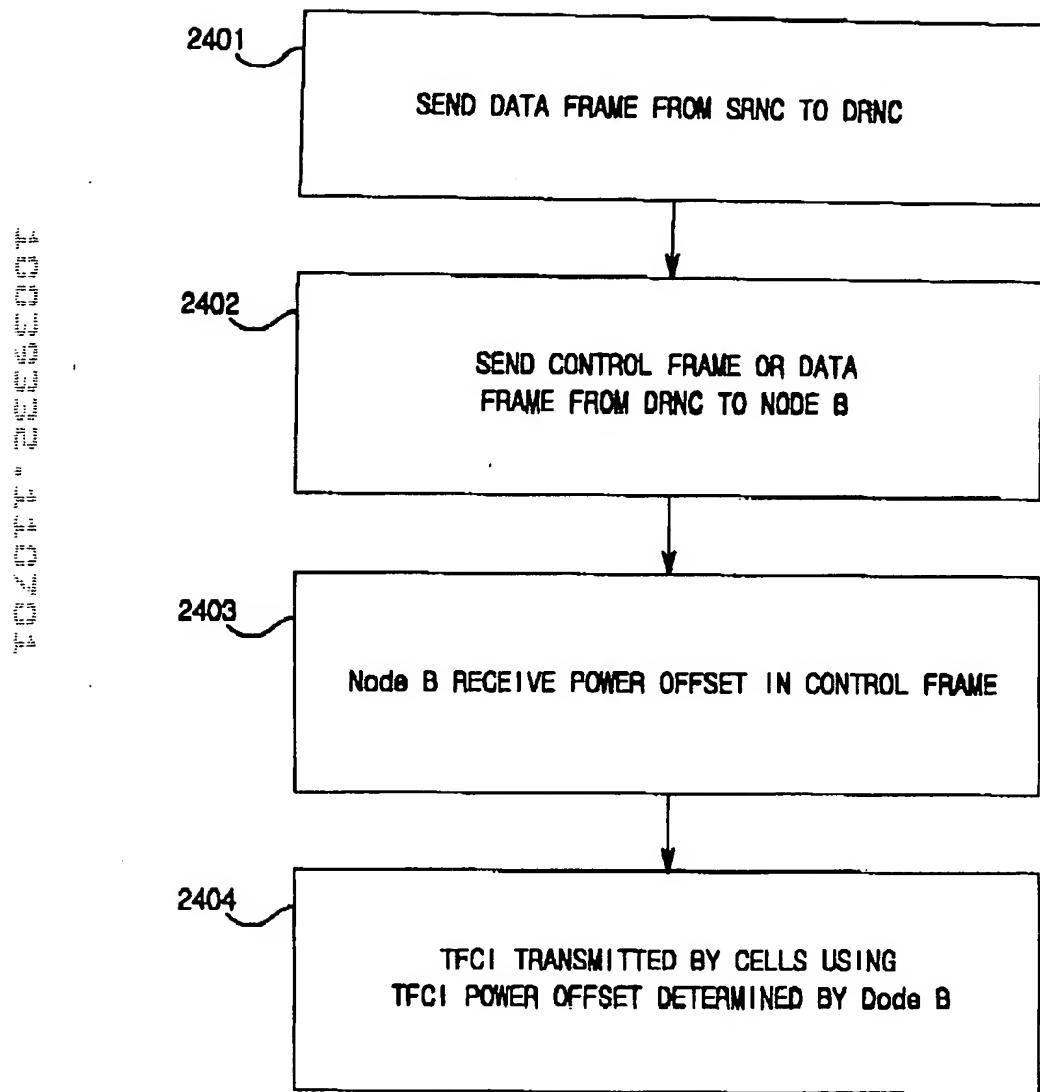


FIG. 24

9. 1.36 RADIO LINK SETUP REQUEST

9. 1.36. 1 FDD message

IE/Group Name	Presence	Range	IE type and reference	Semantic description	Criticality	Assigned criticality
Message Discriminator	M		9.2.1.45		—	
Message Type	M		9.2.1.46		YES	reject
CANC Communication context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62		—	
UL DPCH Information		1			YES	reject
>UL Scrambling code	M		9.2.2.59		—	
>Min UL Channelisation Code Length	M		9.2.2.22		—	
>Max Number of UL DPDCHs	C - CodeLen		9.2.2.21		—	
>puncture limit	M		9.2.1.50	For UL	—	
>TFCI	M		9.2.1.58	For UL	—	
>UL DPCH Slot Format	M		9.2.2.57		—	
>UL SIR Target	M		UL SIR		—	
>Diversity mode	M		9.2.2.9		—	
>SSDT cell ID Length	O		9.2.2.45		—	
>S Field Length	C-FBI		9.2.2.40		—	
DL DPCH Information					YES	reject
>TFCI	M		9.2.1.58	For DL	—	
>DL DPCH Slot Format	M		9.2.2.10		—	
>TFCI signalling mode	M		9.2.2.50		—	
>TFCI presence	C - SlotFormat		9.2.1.57		—	
>Multiplexing Position	M		9.2.2.29		—	
>PDSCH RLID	C-DSCH		RLID		—	
>PDSCH code mapping	C-DSCH		9.2.1.53		—	
>Power Offset Information		1			—	
>>P01	M		Power Offset 9.2.2.29	Power offset for the TFCI bits	—	
>>P02	M		Power Offset 9.2.2.29	Power offset for the TPC bits	—	
>>P03	M		Power Offset 9.2.2.29	Power offset for the pilot bits	—	
>FDD TPC DL Step Size	M		9.2.2.18		—	
The Rest Omitted						

FIG. 25

9. 1.36 RADIO LINK SETUP REQUEST
9. 1.36. 1 FDD message

IE/Group Name	Presence	Range	IE type and reference	Semantics description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45		—	
Message Type	M		9.2.1.46		YES	reject
CPMC Communication context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62		—	
UL DPCCH Information		1			YES	reject
>UL Scrambling code	M		9.2.2.59		—	
>UL Channelisation Code Length	M		9.2.2.22		—	
>Max Number of UL DPDCHs	C - CodeLen		9.2.2.21		—	
>Puncture Pattern	M		9.2.1.50	For UL	—	
>TFCI	M		9.2.1.58	For UL	—	
>UL DPCCH Slot Format	M		9.2.2.57		—	
>UL SIR Target	M		UL SIR		—	
>Diversity mode	M		9.2.2.9		—	
>SSOT call ID Length	O		9.2.2.45		—	
>S Field Length	C-FBI		9.2.2.40		—	
DL DPCH Information					YES	reject
>TFCI	M		9.2.1.58	For DL	—	
>DL DPCH Slot Format	M		9.2.2.10		—	
>TFCI signalling mode	M		9.2.2.50		—	
>TFCI presence	C - SlotFormat		9.2.1.57		—	
>Multiplexing Position	M		9.2.2.29		—	
>PDSCH RLID	C-DSCH		RL ID 9.2.1.53		—	
>PDSCH code mapping	C-DSCH		9.2.2.25		—	
>Power Offset Information		1			—	
>>PO1	M		Power Offset 9.2.2.29	Power offset for the TFCI bits	—	
>>PO2	M		Power Offset 9.2.2.29	Power offset for the TPC bits	—	
>>PO3	M		Power Offset 9.2.2.29	Power offset for the pilot bits	—	
>>PO4	M		Power Offset 9.2.2.29	Power offset for the TFCI bits for DSCH	—	

The Rest Omitted

FIG. 26

9. 1.47 RADIO LINK RECONFIGURATION REQUEST

9. 1.47. 1 FDD message

IE/Group Name	Presence	Range	IE type and Reference	Semantics Description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45		—	
Message Type	M		9.2.1.46		YES	reject
Node B Communication Context ID	M		9.2.1.48	The reserved value "ALL NBCC" shall not be used.	YES	reject
Transaction ID	M		9.2.1.62		—	
UL DPCH Information		0..1			YES	reject
>TFCI	O		9.2.1.58	For the UL.	—	
DL DPCH Information		0..1			YES	reject
>TFCI	O		9.2.1.58	For the UL.	—	
>TFCI Signalling Mode	O		9.2.2.50		—	
>Limited Power Increase	O				—	
>Power Offset Information		1			—	
>>P01	M		Power Offset 9.2.2.29	Power offset for the TFCI bits	—	
>>P04	M		Power Offset 9.2.2.29	Power offset for the TFCI bits for DSCH	—	

The Rest Omitted

FIG. 27